University of Bridgeport

INTRO TO VLSI DESIGN
CPE 448

LAB
ASSIGNMENT-3
5%

VLSI LAB
HANDOUT 1 and 3

Due On: October 16, 2002
Complete the LAB work as described and staged in the LAB Handouts 1 and 2.

1. Design Architect to design the NAND circuit. [LAB-1 pages 1 thru 5]
2. Use Accusim for the Analog Simulation of the circuit [LAB-2]
3. Use IC Station for the layout of the NAND circuit. [LAB-1 pages 5 thru END]